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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/811,698	03/29/2004	Masaki Yamamoto	9281-4784	1355

7590 07/26/2006

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EXAMINER

TRAN, TRANG U

ART UNIT	PAPER NUMBER
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2622

DATE MAILED: 07/26/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/811,698

Applicant(s)

YAMAMOTO, MASAKI

Examiner

Trang U. Tran

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 29 March 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-4 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1 and 2 is/are rejected.
- 7) ☒ Claim(s) 3 and 4 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>3/29/2004</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

2. Claims 1-2 are rejected under 35 U.S.C. 102(a) as being anticipate by the admitted prior art (Fig. 2, pages 1-4 of the Specification).

In considering claim 1, the admitted prior art (Fig. 2, pages 1-4 of the Specification) discloses all the claimed subject matter, note 1) the claimed an input circuit of a television tuner (Fig. 2), comprising a first input terminal to which a television signal is input is met by the first input terminal 51 (Fig. 2, page 1, lines 9-18), 2) the claimed a second input terminal to which an FM broadcasting signal is input is met by the second input terminal 61 (Fig. 2, page 1, line 26 to page 2, line 7), 3) the claimed an input tuning circuit tuned to the television signal and the FM broadcasting signal is met by the input tuning circuit 57 (page 1, line 9 to page 3, line 20), 4) the claimed wherein the first input terminal is connected to an the input end of the input tuning circuit is met by is met by the first input terminal 51 (Fig. 2, page 1, lines 9-18), 5) the claimed first and second diodes serially connected between the second input terminal and the input end of the input tuning circuit and simultaneously switched on or off is met by the switched diodes 64 and 63 (page 1, line 26 to page 3, line 20), 6) the claimed wherein the first diode is input to the input end of the input tuning circuit is met by the switched

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diode 64 (page 1, line 26 to page 3, line 20), 7) the claimed wherein the second diode is provided at the side of the second input terminal is met by the switched diode 63 (page 1, line 26 to page 3, line 20), 8) the claimed an inductance element interposed between the second input terminal and the second diode is met by the inductance element 62 (page 1, line 26 to page 2, line 7), and 9) the claimed a first capacitance element that forms a low pass filter together with the inductance element when the second diode is switched on and is connected between a connection point of the first diode and the second diode and a ground is met by the capacitance element 72 (page 1, line 26 to page 3, line 20).

In considering claim 2, the admitted prior art (Fig. 2, pages 1-4 of the Specification) discloses all the claimed subject matter, note 1) the claimed further comprising a third diode that is switched on or off simultaneously with first and second switches, and is interposed between the inductance element and the second diode is met by the varactor diode 65 (page 2, line 12 to page 3, line 20), and 2) the claimed a second capacitance element that forms a low pass filter together with the inductance element and the first capacitance element when the second and third diodes are switched on and is connected between a connection point of the second diode and the third diode and the ground is met by the capacitance which parallel to the voltage dividing circuit 60 (page 2, line 12 to page 3, line 20).

Allowable Subject Matter

3. Claims 3-4 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Yamamoto et al. (US Patent No. 6,914,639 B2) disclose combination tuner capable of receiving television signal and FM signal.

Yamamoto et al. (US Patent No. 6,351,294 B1) disclose television signal receiving tuner capable of receiving FM broadcasting signals without being affected by other interference signals.

Endo (US Patent No. 6,344,881 B1) discloses television tuner capable of receiving CATV broadcasting signal and ground wave FM broadcasting signal.

Ohira (US Patent No. 6,342,928 B1) discloses receiver having a tuning circuit with a selectable input.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Trang U. Tran whose telephone number is (571) 272-7358. The examiner can normally be reached on 8:00 AM - 5:30 PM, Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David L. Ometz can be reached on (571) 272-7593. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

TT
July 20, 2006



Trang U. Tran
Examiner
Art Unit 2622